

IN THE SPECIFICATION

Amendments to the Specification:

Please amend the paragraph beginning at CROSS-REFERENCE TO RELATED APPLICATIONS (Page 1, Paragraph 1), with the following rewritten paragraph:

Please amend as follows:

-- The present application claims priority under 35 USC § 120, as a Divisional Application from U.S. Application No. 09/377,299, now US Patent No. 6,665,796. This application is also related to U.S. Patent Application Serial No. 09/377,343, entitled Microprocessor in Which Logic Changes During Execution, U.S. Patent Application Serial No. 09/377,298, entitled Execution of Instructions Using Op Code Lengths Longer Than Standard Op Code Lengths to Encode Data, U.S. Patent Application Serial No. 09/376,655, entitled Secure Program Execution Using Instruction Buffer Interdependencies, U.S. Patent Application Serial No. 09/376,654, entitled Secure Program Execution Depending on Predictable Error Correction, U.S. Patent Application Serial No. 09/377,344, entitled Logic Block Used to Check Instruction Buffer Configuration, and U.S. Patent No. 6,308,256, entitled Secure Execution of Program Instructions Provided by Network Interactions with Processor, all invented by Alan C. Folmsbee and commonly assigned, share common subject matter. These applications are incorporated by reference herein in their entirety. --

In the Title:

Please amend the title to "Methods of Microprocessor Instruction Result Obfuscation"